

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	7432	(bidirection\$4 bi near2 direction\$4) with buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/16 21:26
S2	37	S1 and (ring near2 bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/17 00:12
S3	2	"5428800".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/16 21:37
S4	2	"5859545".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/16 21:45
S5	2	"5132987".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/16 21:48
S6	8	"2139008".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/16 21:48
S7	1420	buffer and (ring near2 bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/17 00:13
S8	121	buffer with (ring near2 bus)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/17 00:13

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bidirectional buffer

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Bidirectional Buffer

There exists a simulation model (Google "Ben Cohen" switch model vhd) of a switching **bidirectional** wire/buffer, but it is not synthesizable, ...
www.edaboard.com/ftopic271468.html - 20k - [Cached](#) - [Similar pages](#)

8 BIT BI-DIRECTIONAL BUFFER - DP8304BN

Order '8 bit **bi-directional buffer** - DP8304BN' online from Digi-Key. Manufactured by National Semiconductor. Digi-Key part number DP8304BN-ND.
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CMOS bidirectional buffer for mixed voltage applications - US ...

CMOS **bidirectional buffer** for mixed voltage applications - US Patent 5917348 from Patent Storm. In a preferred embodiment of the present invention, ...
www.patentstorm.us/patents/5917348-claims.html - 23k - [Cached](#) - [Similar pages](#)

[PDF] A 1-Gb/s Bidirectional I/O Buffer Using The Current-Mode Scheme ...

bidirectional buffer showed a measured effective bandwidth. of 1.0 Gb/s [5]. ... of the power of the **bidirectional buffer** is consumed in driving ...
ieeexplore.ieee.org/iel4/4/16274/00753686.pdf?arnumber=753686 - [Similar pages](#)

Reduction of RLC Tree Delay Using Bidirectional Buffer Repeater ...

In this paper, we propose a **bidirectional buffer** repeater insertion to reduce the RLC tree delay in multi-source multi-sink systems which involve four ...
portal.acm.org/citation.cfm?id=1156426.1156962&coll=Portal&dl=GUIDE&CFID=15151515&CFTOKEN... - [Similar pages](#)

(WO/2006/068938) BI-DIRECTIONAL BUFFER FOR INTERFACING TEST SYSTEM ...

Title: **BI-DIRECTIONAL BUFFER FOR INTERFACING TEST SYSTEM CHANNEL**.
Abstract: An emitter follower or source follower transistor is provided in the channel ...
www.wipo.org/pctdb/en/wo.jsp?wo=2006068938 - 14k - [Cached](#) - [Similar pages](#)

[PDF] Reduction of RLC Tree Delay Using Bidirectional Buffer Repeater ...

bidirectional buffer repeater insertion target multi-. source multi-sink systems. compute for the **bidirectional buffer** repeater location. ...
doi.ieeeecomputersociety.org/10.1109/ICICIC.2006.337 - [Similar pages](#)

verilog code for bidirectional buffer

hi, can you please send me a verilog programme for **bidirectional buffer** with suitable explanation if needed. thanks in anticipation regards Fatima.
www.vlsibank.com/sessionspage.asp?titl_id=13314 - 7k - [Cached](#) - [Similar pages](#)

Actel KB: KI47146: The loopback option in the bi-directional ...

SmartTime still does not show the path through the **Bi-directional buffer**. Description: SmartTime allows you to specify if you need to use loopback in ...
www.actel.com/kb/article.aspx?id=KI47146 - 21k - [Cached](#) - [Similar pages](#)

Examination and A tentative a Bidirectional Buffer 3 states ...

By basing you on the results obtained, you can build the table of operation of the **bidirectional buffer** indicated figure 17 which can be summarized ...

www.premiumwanadoo.com/daniel.robert9/anglais/Digit/Pratique/Digit_12PS.html - 55k -
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